

## CYT8117 1A Positive Voltage Regulator



## General Description

The CYT8117-ADJ and CYT8117-1.2, 1.5, -1.8, -2.5, -3.3 and -5.0 are low dropout three-terminal regulators with 1A output current capability. These devices have been optimized for low voltage where transient response and minimum input voltage are critical.

On-chip thermal limiting provides protection against any combination of overload and ambient temperatures that would create excessive junction temperatures.

Unlike PNP type regulators where up to 10% of the output current is wasted as quiescent current, the quiescent current of the CYT8117 flows into the load, increasing efficiency.

The CYT8117 series regulators are available in the industry-standard SOT-223 power packages.

## Electric Characteristics

Unless otherwise stated,  $T_A=25^{\circ}\text{C}$ , Normal junction temperature range  $-40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ .

| Description               | symbol            | Conditions                                                                                                                                                                                                                          | Min.           | Typ.                 | Max.                 | Unit          |
|---------------------------|-------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|----------------------|----------------------|---------------|
| Reference Voltage         | $V_{\text{REF}}$  | CYT8117-ADJ, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}-V_{\text{OUT}}=2\text{V}$ ,<br>$T_J=25^{\circ}\text{C}$ , $10\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $1.4\text{V}\leq V_{\text{IN}}-V_{\text{OUT}}\leq 10\text{V}$ | 1.231<br>1.225 | 1.250<br>1.250       | 1.268<br>1.275       | V             |
| Output Voltage            | $V_{\text{OUT}}$  | CYT8117-1.2, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}=3.2\text{V}$ ,<br>$T_J=25^{\circ}\text{C}$ , $10\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $3.0\text{V}\leq V_{\text{IN}}\leq 10\text{V}$                             | 1.176<br>1.152 | 1.200<br>1.200       | 1.224<br>1.248       | V             |
|                           |                   | CYT8117-1.5, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}=3.5\text{V}$ ,<br>$T_J=25^{\circ}\text{C}$ , $10\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $3.0\text{V}\leq V_{\text{IN}}\leq 10\text{V}$                             | 1.477<br>1.470 | 1.500<br>1.500       | 1.522<br>1.530       | V             |
|                           |                   | CYT8117-1.8, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}=3.8\text{V}$ ,<br>$T_J=25^{\circ}\text{C}$ , $0\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $3.2\text{V}\leq V_{\text{IN}}\leq 10\text{V}$                              | 1.773<br>1.746 | 1.800<br>1.800       | 1.827<br>1.854       | V             |
|                           |                   | CYT8117-2.5, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}=4.5\text{V}$ , $T_J=25^{\circ}\text{C}$ ,<br>$0\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $3.9\text{V}\leq V_{\text{IN}}\leq 10\text{V}$                              | 2.462<br>2.450 | 2.500<br>2.500       | 2.538<br>2.550       | V             |
|                           |                   | CYT8117-3.3, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}=5.0\text{V}$ ,<br>$T_J=25^{\circ}\text{C}$ , $0\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $4.75\text{V}\leq V_{\text{IN}}\leq 10\text{V}$                             | 3.250<br>3.235 | 3.300<br>3.300       | 3.349<br>3.365       | V             |
|                           |                   | CYT8117-5.0, $I_{\text{OUT}}=10\text{mA}$ , $V_{\text{IN}}=7.0\text{V}$ , $T_J=25^{\circ}\text{C}$ ,<br>$0\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $6.5\text{V}\leq V_{\text{IN}}\leq 12\text{V}$                              | 4.925<br>4.900 | 5.500<br>5.000       | 5.075<br>5.100       | V             |
| Output Voltage Stability  | $T_{\text{SOUT}}$ | -                                                                                                                                                                                                                                   | -              | 0.3                  | -                    | %             |
| Line Regulation           | $R_{\text{LINE}}$ | $V_{\text{INMIN}}\leq V_{\text{IN}}\leq 12\text{V}$ ,<br>$V_{\text{OUT}}=\text{Fixed/Adj}$ , $I_{\text{OUT}}=10\text{mA}$                                                                                                           | -              | 6                    | 15                   | mV            |
| Load Regulation           | $R_{\text{LOAD}}$ | $10\text{mA}\leq I_{\text{OUT}}\leq 1\text{A}$ , $V_{\text{OUT}}=\text{Fixed/Adj}$                                                                                                                                                  | -              | 6                    | 18                   | mV            |
| Dropout Voltage           | $V_{\text{Drop}}$ | $I_{\text{OUT}}=100\text{mA}$<br>$I_{\text{OUT}}=500\text{mA}$<br>$I_{\text{OUT}}=1\text{A}$                                                                                                                                        | -              | 1.00<br>1.05<br>1.20 | 1.20<br>1.25<br>1.30 | V             |
| Quiescent Current         | $I_{\text{Q}}$    | $4.25\text{V}\leq V_{\text{IN}}\leq 6.5\text{V}$                                                                                                                                                                                    | -              | 5                    | 10                   | mA            |
| Ripple Rejection          | $P_{\text{SRR}}$  | $f_{\text{RIPPLE}}=120\text{Hz}$ , $(V_{\text{IN}}-V_{\text{OUT}})=3\text{V}$ ,<br>$V_{\text{RIPPLE}}=1\text{V}_{\text{PP}}$                                                                                                        | 50             | 60                   | -                    | dB            |
| Adjust Pin Current        | $I_{\text{ADJ}}$  | -                                                                                                                                                                                                                                   | -              | 60                   | 120                  | $\mu\text{A}$ |
| Adjust Pin Current Change | -                 | $0\text{mA}\leq I_{\text{OUT}}\leq 800\text{mA}$ , $1.4\text{V}\leq V_{\text{IN}}-V_{\text{OUT}}\leq 10\text{V}$                                                                                                                    | -              | 0.2                  | 5                    | $\mu\text{A}$ |

## Electrical Characteristics (Continue)

| Description                          | symbol        | Conditions                                            | Min. | Typ.  | Max. | Unit |
|--------------------------------------|---------------|-------------------------------------------------------|------|-------|------|------|
| Temperature Protection Point         | $T_{SD}$      | -                                                     | -    | 150   | -    | °C   |
| Current Limit Protection Point       | $I_{limit}$   | -                                                     | 1.4  | 1.6   | 1.8  | A    |
| Temperature Stability                | -             | -                                                     | -    | 0.5   | -    | %    |
| Long-Term Stability                  | -             | $T_A=125^{\circ}\text{C}$ , 1000Hrs                   | -    | 0.3   | -    | %    |
| RMS Output Noise                     | -             | % of $V_{OUT}$ , $10\text{Hz}\leq f\leq 10\text{kHz}$ | -    | 0.005 | -    | %    |
| Thermal Resistance, Finless          | $\theta_{JA}$ | -                                                     | -    | 120   | -    | °C/W |
| Thermal Resistance, Junction to Case | $\theta_{JC}$ | -                                                     | -    | 15    | -    | °C/W |

## Absolute Maximum Ratings

| Parameter                             | Min. | Max.         | Unit |
|---------------------------------------|------|--------------|------|
| $V_{IN}$                              | -    | 18           | V    |
| $(V_{IN}-V_{OUT}) \cdot I_{OUT}$      | -    | See Figure 1 | -    |
| Operating Junction Temperature Range  | 0    | 125          | °C   |
| Storage Temperature Range             | -65  | 150          | °C   |
| Lead Temperature (Soldering, 10 sec.) | -    | 300          | °C   |

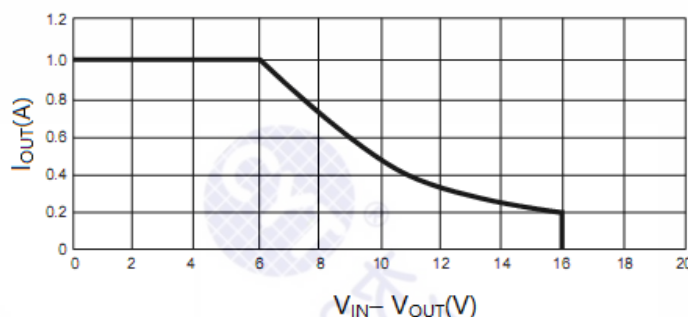
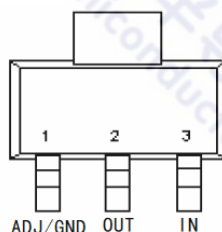


Figure 1. Absolute Maximum Safe Operating Area

## Pin Diagram(Top View)



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## Block Diagram

